

ABSTRACT

A method of $\pi/4$ -shift QPSK modulation and $\pi/4$ -shift QPSK modulator are provided. The modulator address symbols from a Precursor section of a register to a first memory portion and from a Postcursor section of the register to a second memory portion at odd times and, address symbols from the Precursor section to said second memory portion and from said Postcursor section to said first memory portion at even times. The modulator uses reversed addressing for Postcursor symbols in accessing the said first and second memory portions.